IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re the Application of: Hidehiko KIRA, et al.

Group Art Unit: 2822

Serial No.: 08/897,953

Examiner: David E. GRAYBILL

Filed: **July 24, 1997**

P.T.O. Confirmation No.: 5157

For: METHOD AND SYSTEM FOR FABRICATING A SEMICONDUCTOR DEVICE

AMENDMENT in RCE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 950107A

October 31, 2007

Sir:

In response to the Final Office Action dated October 2, 2007, an RCE being filed herewith, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.